

**REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2188**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	10/821,370	§	Examiner:	Eland, Shawn
Filed:	April 9, 2004	§	Group/Art Unit:	2188
Inventor(s):		§	Atty. Dkt. No:	5181-99901
Landin et al.		§	Conf. No.	1207
		§		
		§		
		§		
Title:	MULTI-NODE	§		
	COMPUTER SYSTEM	§		
	EMPLOYING MULTIPLE	§		
	MEMORY RESPONSE	§		
	STATES	§		
		§		
		§		

**AMENDMENT; RESPONSE TO FINAL OFFICE ACTION OF
June 29, 2007**

MAIL STOP AF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of June 29, 2007, to further highlight why the application is in condition for allowance.